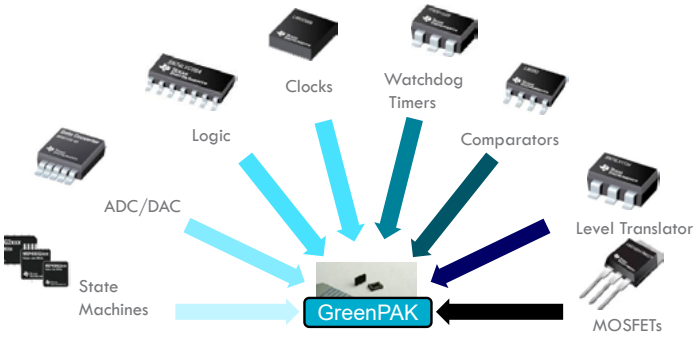
GreenPAK - One IC to reduce your board space & part count

The GreenPAK Series is an IC similar to a CPLD but also containing mixed signal functions. One Device can replace discrete logic, implement custom functions, reduce size, save cost, and they can provide design security and global traceability.

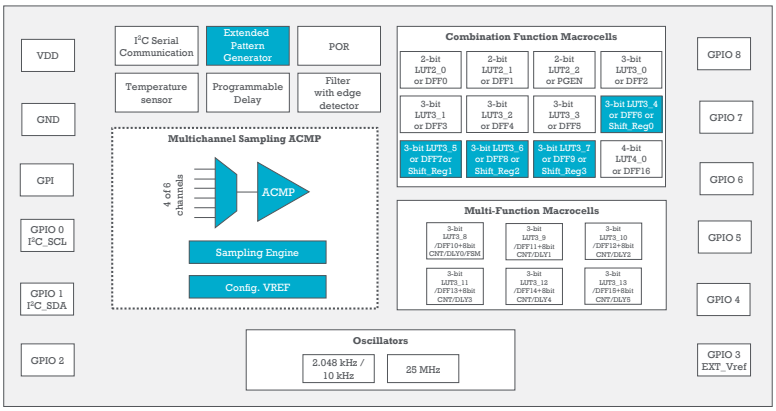
- Reduce power consumption (200nA-300uA)
- Faster time-to-market
- Secure (unique serial Number)
- No NRE
- Small package TQFN 0.4 mm pitch
- Low cost
- In-system programmable with I2C
- Multi programmable / one time programmable
- Free software GreenPak Designer (Win,iOS and Linux)
- Option for SPICE Simulation
- Youtube training videos are available
- Dialog can create designs from full schematic reviews



What is inside? Almost Everything for your MCU

They contain logic blocks (LUT's, DFF's), timing blocks (oscillators, counters, delays) and analog blocks (ACMP's, ADC's, DAC's, temp sensors) as well as standard bus interfaces (SPI, I2C), hardware-based state machines, load switches, LDO's, op amps, high voltage VDD rails, and many other GPIO options.

Lots of functions are possible in a tiny space... for example the SLG46811 – 12 pin STQFN 1.6 x 1.6mm² Package



Variety of small Packages

1.0 mm x 1.2 mm STQFN 8-pin package	1.6 mm x 1.6 mm STQFN 12-pin package	1.6 mm x 2.0 mm STQFN 14-pin package	2.0 mm x 2.2 mm STQFN 14-pin package	1.6 mm x 2.5 mm STQFN 14-pin package	2.0 mm x 3.0 mm STQFN 20-pin package	2.0 mm x 2.2 mm MSTQFN 22-pin package

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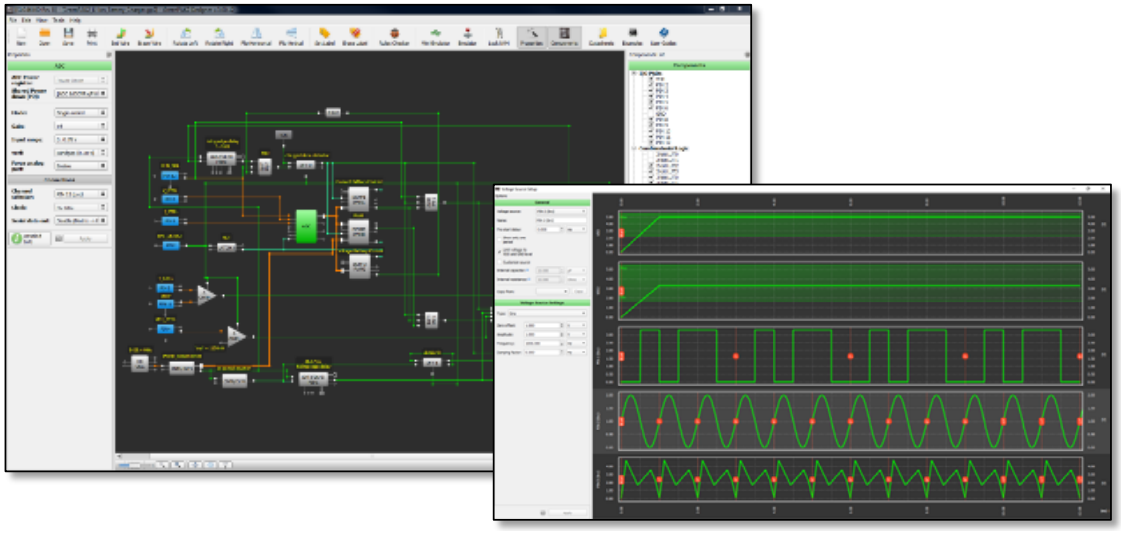
Design-in Expertise and Service

Design a GreenPAK with everything you need!

GreenPAK Designer development software enables a completely graphical design process, requiring no programming language or compiler allowing a designer to configure, program, and test custom GreenPAK samples in minutes. The Designer Software is available for Windows, iOS, and Linux operating systems.

- Schematic capture-like design and routing
- Entire component library showing available resources for each device
- Easy component configuration
- Example projects and support documentation



Inside the GreenPAK Designer software, there is an option for SPICE simulation. This includes not only the internal GreenPAK blocks but you have the option to add some external components as well. This is the fastest way to verify your design because up to this point, you do not need any hardware. You can work on your laptop and design anywhere.

Easy-to-use GUI-based Design Software
https://www.dialog-semiconductor.com/greenpak-designer-software

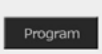
Application Notes (over 300 app notes including sample design files)
https://www.dialog-semiconductor.com/greenpak-application-notes

Training Videos:
https://www.dialog-semiconductor.com/videos/training-videos

GreenPAK Cookbook, this compendium includes tips and tricks to help create designs
https://www.dialog-semiconductor.com/greenpak-cookbook

Dialog can then create your design – absolutely free of charge.

- Design creation is free
- Samples are free
- No Production commitment
- MOQ is 3k pcs
- 4-6 Week Production Lead-Time
- Custom Datasheet
- Specific Part number
- Customer-only-Product

Design in Minutes	Prototype in Hours		Production in Days
Step1	Step 2	Step 3	Step 4
			
Place unprogrammed GreenPak into socket	Design your IC with GreenPak GUI	Click Program to freeze GreenPAK's OTP NVM	Your custom IC is ready for use

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