Advanced PMIC with 3 Bucks, 2 LDOs and Load Bypass Switch

Cost Optimized 5V PMIC in Small WLCSP Package

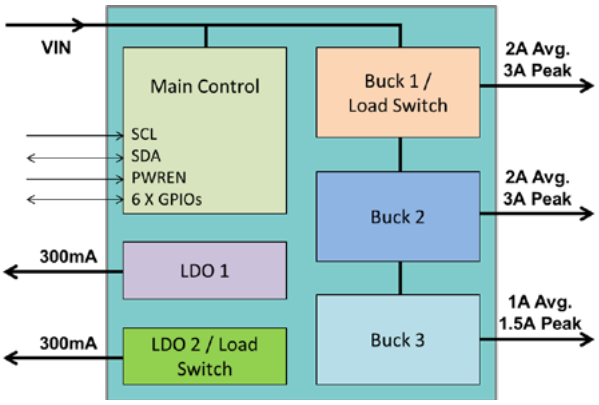
The ACT88327 PMIC is an integrated ActiveCiPSTM power management integrated circuit. It powers a wide range of processors, including solid-state drive applications, video processors, FPGA's, wearables, peripherals and microcontrollers. The ACT88327 is optimized for SSD and FPGA applications. It is highly flexible and can be reconfigured via I^2C for multiple applications without the need for PCB changes. The low external component count and high configurability significantly speeds time to market. Examples of configurable options include output voltage, start-up time, slew rate, system level sequencing, switching frequency, sleep modes, operating modes etc. ACT88327 is programmed at the factory with a default configuration. These settings can be optimized for a specific design through the I^2C interface. The ACT88327 is available in several default configuration.

High Integration PMIC – Key Features:

- 2.7V ~ 5.3V Input Voltage Range
- Buck 1: 3A Buck / Load Switch
- Buck 2: 3A Buck Optimized for Low Output Voltage
- Buck 3: 1.5A Peak Buck for I/O
- All Bucks Work with 0.47uH Inductors
- 2 X 300mA LDOs, LDO2 can be configured as Load Switch

High System Configurability:

- I^2C Serial Interface for Monitoring and Control
- 6 GPIOs
- Interrupt Controller for Faults & Status Monitoring
- Highly Configurable for Regulation Voltages, Power Sequencing (Up & Down) and GPIO Functionality
- Multiple Sleep Mode
- 2.6mm x 2.2mm WLCSP Package



Applications

- Solid-State Drives (SSD)
- FPGA
- Computer Vision
- Portable Audio / Video

INELTEK GmbH · Hauptstrasse 45 · 89522 Heidenheim · Germany
Phone +49 7321 9385 0 · Fax +49 7321 9385 95 · info@ineltek.com · www.ineltek.com

Design-in Expertise and Service