

Ineltek GmbH
an ATeG Company
Hauptstrasse 45
89522 Heidenheim
Deutschland

Unterpremstätten, Jan.13th, 2011

Subject: Second source for wafer fabrication PCN 03a-2010

Dear Sirs,

In order to reasonably protect the supply of your product

AS3685

and to assure additional flexibility and capacity needed to further improve responsiveness and on time delivery to our customers, austriamicrosystems AG is currently qualifying a supplemental source for wafer fabrication. This will enable us to better support the ever increasing demand in today's dynamic market environment and to have an alternative and ready solution for manufacturing issues like tight-line capacity situation, on-allocation situation, and any other manufacturing issues that can arise and potentially prevent austriamicrosystems AG from meeting the customer demand on-time.

The above mentioned device, previously qualified at austriamicrosystems Fab B, will also be qualified at TSMC.

TSMC is a qualified manufacturing partner of austriamicrosystems AG for years. TSMC is certified in accordance with the ISO9001 and the automotive quality system requirements, TS16949. The technology (mixed signal 0,35um CMOS) is automotive qualified and fully compatible to austriamicrosystems technology.

This change covers only the wafer fabrication of the product. Testing and backend services will all be unchanged.

Process Flow Matrix:

	Current supply chain	New supply chain
Wafer Fabrication	austriamicrosystems - Austria	austriamicrosystems – Austria or TSMC
Wafer Probe Testing	unchanged - austriamicrosystems - Austria	unchanged - austriamicrosystems - Austria
Assembly	unchanged – at qualified assembly partners	unchanged – at qualified assembly partners
Final Testing and Packing	unchanged - austriamicrosystems – Austria or austriamicrosystems - Philippines	unchanged - austriamicrosystems – Austria or austriamicrosystems - Philippines

Advantages:

austriamicrosystems' 0,35 CMOS technology is a TSMC licensed technology and matches therefore very well with TSMC's mixed signal CMOS process.

The following product will be affected:

AS3685 (17673xxx)

Timing of change:

The supplemental wafer fabrication is qualified.

Set of qualification tests:

austriamicrosystems is currently qualifying the additional wafer fabrication according to JEDEC standards. Detailed activities and an actual status of the qualification are available upon request.

If you do have further questions please do not hesitate to contact us.

Please be advised that unless we receive your written refusal concerning this PCN within 30 days, the PCN shall be deemed accepted.

Best regards,

austriamicrosystems AG

A handwritten signature in black ink, appearing to read 'Herwig Klimesch'.

Herwig Klimesch
Director Quality & Environment