

Welcome to the new edition of the Ineltek NEWSLETTER. It contains news, facts and information to help you to obtain the best out of our products and services.

Our new issue contains information

## Product Information October 2011

- **austriamicrosystems Announces High-Efficiency 3A Step-Down Converter AS1328**
- **Atmel and Redpine Signals Collaborate to Provide Ultra-low Power 802.11n Wi-Fi Capability for AVR and ARM-based MCUs**
- **Epson`s S1D13781 WQVGA Graphics Controller**

Product series: AS13xx

### **austriamicrosystems Announces High-Efficiency 3A Step-Down Converter AS1328**

Unterpremstaetten, Austria – austriamicrosystems (SIX: AMS), a leading global designer and manufacturer of high-performance analog ICs, has announced the AS1328 high-efficiency 1.5 MHz synchronous DC-DC step-down converter. This buck regulator features efficiencies up to 96%, excellent light load efficiencies and with a quiescent current of only 25  $\mu$ A.

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Product series: Wi-Fi Starter Kits RS-xxx

### **Atmel and Redpine Signals Collaborate to Provide Ultra-low Power 802.11n Wi-Fi Capability for AVR and ARM-based MCUs**

Atmel customers can add 802.11a/b/g/n Wi-Fi connectivity using two easy-to-use solutions from Redpine Signals, which are optimized for Atmel MCUs. The fully-certified Redpine Connect-io-n Wi-Fi modules with integrated TCP/IP stack are ideal for Atmel AVR XMEGA and AVR UC3 MCUs, and Atmel SAM3 ARM-based MCUs, while the high-performance n-Link modules with host-managed TCP/IP stack are ideal for Atmel SAM9 ARM-based MCUs. Redpine's Connect-io-n and n-Link solutions connect to Atmel MCUs through UART, SPI and SDIO interfaces and offer up to 65Mbps of physical bit rate and up to 45Mbps of data throughput on SDIO interfaces.

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Product series: S1D13781

## Epson`s S1D13781 WQVGA Graphics Controller

The S1D13781 is a simple, multi-purpose Graphics LCD Controller with 384KByte embedded SRAM display buffer which supports both RGB interface TFT and CSTN panels. The S1D13781 supports most popular CPU interfaces in both 8/16-bit and Direct/Indirect variations. The embedded display buffer allows WQVGA up to 480x272 at 24bpp or 800x480 8bpp for single layer display, or 480x272 at 16bpp(Main Layer) and 480x272 at 8bpp(PIP Layer) for two layer display.



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For more information regarding samples, prices and lead times for the above products please send your inquiry to: [info@ineltek.com](mailto:info@ineltek.com)

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