

DVI + HDCP Compliant Receiver

EP169

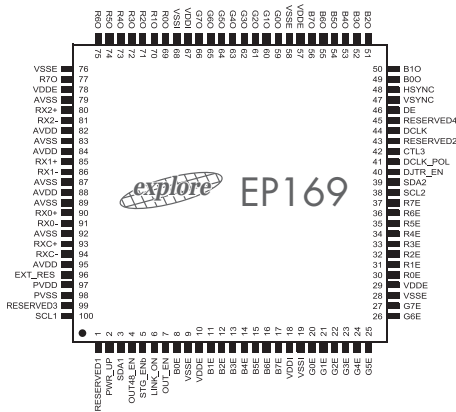
General Description

The EP169 is a low cost DVI receiver integrated with HDCP (High-bandwidth Digital Content Protection) engine in 100-pin LQFP/TQFP package. It is compliant to DVI Revision 1.0 and HDCP Revision 1.0 specifications. HDCP keys are encrypted and put in an external EEPROM which can be the same EEPROM containing the EDID data for DDC. This provides users with flexibility in programming the HDCP keys without additional system cost for HDCP key storage. The chip supports display resolution from VGA to UXGA (25 - 165MHz) in 1 or 2 pixels/clock mode. The on-chip Link On detection circuit works even when the chip is put in Power Down mode.

Features

- ◆ DVI specification 1.0 compliant
- ◆ HDCP specification 1.0 compliant
- ◆ Operation pixel frequency range: 25MHz - 165MHz
- ◆ Encrypted HDCP keys store in external EEPROM which can be the same EEPROM containing the EDID data for DDC
- ◆ PLL requires no external components
- ◆ High skew tolerance: 1 full input clock cycle
- ◆ H/V sync de-jittering
- ◆ Low current consumption in Power Down mode
- ◆ Link On detection even in Power Down mode
- ◆ Controllable tri-state for output port
- ◆ Backward compatible with EP161
- ◆ Single 3.3V CMOS design
- ◆ 100-pin LQFP/TQFP

Pin Diagram



Explore Microelectronics Inc.