165MHz 24Bit Color DVI Compliant Chip Set

EP161

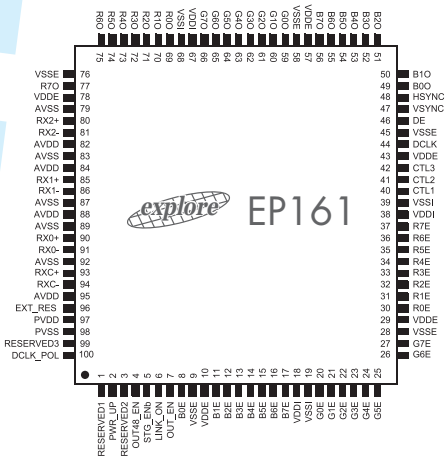
General Description

The EP161 is a low cost receiver for DVI Rev.1.0 compliant in 100-pin TQFP package. The chip set supports display resolution from VGA to UXGA (25-165MHz) with up to 24bit/pixel data in 1 or 2 PIXEL/CLOCK mode. The built-in PLL based on proprietary hi-Frequency PLL technologies used in the EP Series products requires no external component.The on-chip Link On detection circuit works even when the chip is put in Power Down mode.

Features

- Compliant with DVI Rev1.0 specification
- Operation pixel frequency range: 25-165MHz
- PLL requires No External Components
- Single 3.3V Low Power CMOS design
- High skew tolerance:1 full input clock cycle
- Low current consumption in Power Down mode
- Link On detection even in Power Down mode
- Controllable tri-state for output port
- 100-pin TQFP
- Programmable Single/Duel Pixel Mode

Pin Diagram



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